

SUBSTRATE NOISE COUPLING IN RFICS%0A

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In this book a substrate aware design flow is built, calibrated to silicon and used as part of the design and validation flows to uncover and fix substrate coupling problems in RF ICs. The flow is used to develop a comprehensive RF substrate noise isolation design guide to be used by RF designers during the early planning,

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Substrate Coupling Noise Modeling and Mitigation Techniques

Index Terms Substrate coupling noise, substrate extraction, modeling techniques, noise mitigation techniques, isolation techniques. I. INTRODUCTION THE increased demand for high performance and low power has resulted into integration of RF, digital, and analog circuits together on same substrate. While the dense integration improves the performance substantially and saves additional silicon

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Substrate Coupling Analysis and Noise Reduction Methods

Substrate Coupling Analysis and Noise Reduction Methods substrate coupling is found to be a major obstacle for the advancement of system-on-a chip. Accurate predictions of substrate coupling in large-scale systems demand a modeling algorithm with high speed and high resolution, which are difficult to achieve simultaneously. This thesis provides a comprehensive approach towards predicting

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Substrate Coupling Modeling and Mitigation Techniques

* Afzali-Kusha et al., Substrate Noise Coupling in SoC Design: Modeling, Avoidance, and Validation, in Proceedings of the IEEE , 2006. *Noise coupling from switching portion of an IC

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Substrate Noise Coupling 2017

Roland Jancke, head of the department for design methodology for Fraunhofer's Engineering of Adaptive Systems Division, talks with Semiconductor Engineering about the impact of substrate noise

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Analysis of Substrate Noise Coupling --Experimental Data to Calibrate the Design Flow --Design Guide for Substrate Noise Isolation in RF Applications --On Chip Inductors Design Flow --Case Studies for the Impacts and Remedies of Substrate Noise Coupling --Conclusion and Future Work.

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Noise Coupling is the root-cause of the majority of Systems on Chip (SoC) product fails. The book discusses a breakthrough substrate coupling analysis flow and modelling toolset, addressing the needs of the design community.

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Substrate coupling Wikipedia

Substrate coupling. To elaborate, note that in mixed-signal circuits, both sensitive analog circuits and high-swing high-frequency noise injector digital circuits may be present on the same chip, leading to undesired signal coupling between these two types of circuit via the conductive substrate.

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"Substrate Noise Coupling in RFICs addresses substrate noise coupling in RF and mixed signal ICs when used in a system on chip (SoC) containing digital ICs as well. This trend of integrating RF, mixed signal ICs with large digital ICs is found in many of today's commercial ICs such as single chip Wi-Fi or Bluetooth solutions and is expected to grow rapidly in the future. The book reports modeling and simulation

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Substrate Noise Coupling in RFICs reports silicon measurements, new test and noise isolation structures as well as calibration of a design flow used in the design and debug phases of RFICs. A design guide is articulated to be used by RFIC designers to maximize signal isolation and optimize chip floor plan, power and ground domains. Industrial examples of RFICs are given as demonstration

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Topics include substrate noise propagation, passive isolation structures, noise couple in active devices, measuring the coupling mechanisms in analog/RF circuits, prediction of the impact of substrate noise on analog/RF circuits, and noise coupling in analog/RF systems.

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